

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Chambers, et al. Docket No: TI-30883  
Serial No: 09/902,051 Examiner: Davidson, Dan  
Filed: 7/10/2001 Art Unit: 2651  
For: CMOS DC OFFSET CORRECTION CIRCUIT WITH PROGRAMMABLE HIGH-PASS  
TRANSFER FUNCTION

APR 01 2004

OFFICIAL

# 11  
JMM

NOTICE OF APPEAL FROM THE PRIMARY EXAMINER  
TO THE BOARD OF APPEALS

Assistant Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

CERTIFICATION OF FACSIMILE TRANSMISSION I hereby  
certify that the following papers are being transmitted by  
facsimile to the U.S. Patent and Trademark Office at 703-872-  
9306 on the date shown below:

Tommie Chambers

4-1-04

Applicant hereby appeals to the Board of Appeals from the decision mailed 09/09/03, of the Primary  
Examiner finally rejecting Claims 1-10.

The item(s) checked below are appropriate:

1.  An extension of time to respond to the final rejection

was granted on \_\_\_\_\_.  
 is requested for 1 month.

2.  A timely response to the final rejection has been filed, as provided in  
841 O.G. 1411.

3.  Fee \$330.00:

Not required (Fee paid in prior appeal)  
 The Commissioner of Patents is hereby authorized to charge any  
fees which may be required or credit any overpayment to the deposit  
account of Texas Instruments Incorporated, Account No. 20-0668.  
This form is submitted in duplicate.

04/08/2004 SMOORE 00000001 200668 09902051

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NAME OF INVENTOR(S): Chambers	
TITLE OF INVENTION: CMOS DC OFFSET CORRECTION CIRCUIT WITH PROGRAMMABLE HIGH-PASS TRANSFER FUNCTION	
TI FILE NO.: <b>TI-30883</b>	DEPOSIT ACCT. NO.: <b>20-0668</b>
FAXED: 04/01/2004 DUE: 03/08/2004 ATTY/SEC'D: wds/tc	
RECEIPT DATE & SERIAL NO.: <b>Serial No.: 09/902,051</b> <b>Filing Date: 7/10/2001</b>	

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